

Due: **Friday, September 28 at 9:00 pm.**

Submission: Please hand in a (tidy) paper copy at the beginning of class.

1. Perform the binary addition in the following two problems. Determine whether overflow occurred. Check your answer by converting both of the operands and the result to decimal representation.

$$\begin{array}{r} 101011 \\ +100101 \\ \hline \end{array} \qquad \begin{array}{r} 011001 \\ +100111 \\ \hline \end{array}$$

2. Consider the following binary addition problem that will be computed using a carry-lookahead adder.

$$\begin{array}{r} 1011\ 1001\ 0011\ 1010 \\ +\ 0100\ 1101\ 0101\ 0110 \\ \hline \end{array}$$

Following the format given in a similar example in the eReserve reading (p.247),

- Find g_i and p_i for each bit.
- Find P_0 through P_3 , and G_0 through G_3 , the propagate and generate functions for each 4-bit nibble.
- Finally, find C_1 through C_4 , the carry-out values from each 4-bit nibble.

3. Suppose you had worked out the various P_i , G_i , and C_i values corresponding to a particular carry-lookahead sum, but then your roommate spilled a super caffeine drink on your paper, rendering much of it unreadable. All that is salvagable are the following values, but you also remember that both numbers to be added were positive.

$$\begin{array}{l} P_0 = \quad G_0 = \quad C_1 = \\ P_1 = \quad G_1 = 0 \quad C_2 = 1 \\ P_2 = \quad G_2 = \quad C_3 = 0 \\ P_3 = \quad G_3 = 1 \quad C_4 = \end{array}$$

a. Fill in as many of the missing values as possible. If not enough information is available to be sure about the correct value for one of the missing values, then leave it blank.

b. Give a pair of 16-bit binary numbers that could have resulted in the table given above.

4. Consider a 32-bit adder that is constructed by “cascading” two 16-bit carry-lookahead adders. In other words, the final carry-out of the first 16-bit adder is fed directly to the second 16-bit adder as its initial carry-in signal.

- a. How many gate delays are required before the final carry-out from the full 32-bit adder becomes correct?
- b. How many gate delays are required before the final sum bit from the 32-bit adder becomes correct?

Please justify your answers to both parts of this question.