

CSC211
Laboratory 3

Background Information

See the handouts from previous labs.

Laboratory Exercises

Using TTL logic chips, implement the following circuits:

1. A full-adder. Your circuit should have three inputs (two input bits and a carry-in bit) and two outputs (sum and carry-out).
2. A 2:4 decoder. Your circuit should have two input bits and four output bits. Recall that you will interpret the input as a binary number and “decode” that number by setting only the one corresponding output bit high for any given input.